

**RESPONSE UNDER 37 C.F.R. § 1.111
U.S. APP. NO. 09/078,555**

REMARKS

Claims 1-41 are all the claims pending in the application.

Claims 1-23, 26, 27, 30, 34 and 37 are withdrawn from consideration.

Claims 24, 25, 31-33, and 38-41 are allowed.

Claims 28, 35 and 36 are rejected under 35 U.S.C. § 102(e) as being anticipated by Fukuda et al (U.S. Patent No. 5,491,514). Applicant respectfully traverses Our comments with respect to this rejection are set forth below.

Analysis of the Rejection of Claims 28 and 35-36

In rejecting claim 28, the Examiner cites element 1702 of Fig. 17 of Fukuda et al as teaching the claimed step of controlling the operating mode of the equalizer. Specifically, the Examiner states “means for controlling the operating mode of said adaptive equalizer as a function of the determined DC variation, wherein said received signal includes a field sync signal and wherein said DC variation determining means operates on said field sync signal (see Fig. 17, element 1702 and its description).” Applicant respectfully disagrees with the Examiner’s analysis.

First, Applicant notes that the Examiner has ignored many of the claimed features of the step of controlling the operating mode of the equalizer. For example, the Examiner ignores the claimed feature that the field synchronizing signal comprises a pseudo random number symbol sequence and wherein the processing comprises sampling a part of the pseudo random number

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symbol sequence. Fukuda et al contains no teaching regarding a pseudo random number symbol sequence, let alone that the field synchronizing symbol comprises a pseudo random number symbol sequence. As another example, the Examiner ignores the claimed feature that the determining step further comprises processing the field synchronizing signal to determine the variation of the DC offset in the received signal. Fukuda et al teaches nothing with regard to processing the field synchronizing signal to determine DC offset.

Also, the Examiner implies that element 1702 of Fig. 17 of Fukuda et al teaches controlling the operating mode of an adaptive equalizer. There is no teaching in Fukuda et al of an adaptive equalizer, and therefore no teaching of controlling the operating mode of an adaptive equalizer.

For at least the above reasons, Applicant submits that claim 28 is not anticipated by Fukuda et al.

Regarding claim 35, Applicant submits that Fukuda et al does not teach controlling the operating mode of an adaptive equalizer as required by the claim. Applicant also submits that Fukuda et al does not teach that a DC variation determining means operates on a field sync signal, as also required by claim 35.

Regarding claim 36, which depends from claim 35, Fukuda et al does not teach that the field sync signal comprises a pseudo random number sequence of symbols, and does not teach means for sampling a portion of said sequence of symbols for processing by said DC variation means.

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Furthermore, DC variation determining means in the present invention only determines DC level variation of the received signal, and in particular the received signal is a signal including a field synchronizing signal, while a signal input in a DC level detection circuit (1701) of Fukuda is a digital image signal or a decoded image signal. Therefore, in the composition of determining a DC level, the input signal of the present invention is different from that of Fukuda. In addition, even though Fukuda discloses outputting a mode control signal, Fukuda cannot control the operation mode of the adaptive equalizer as disclosed in the present invention.

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

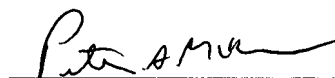
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